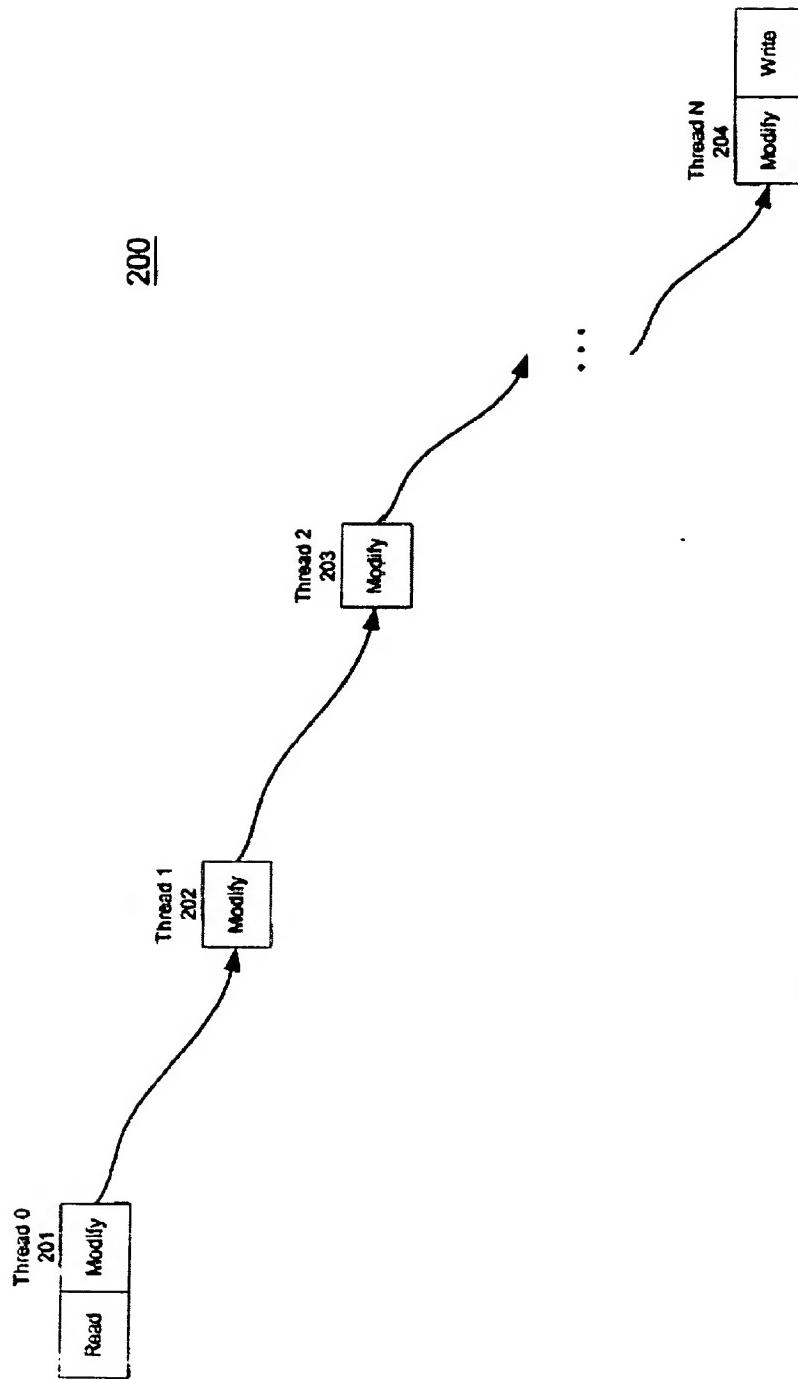
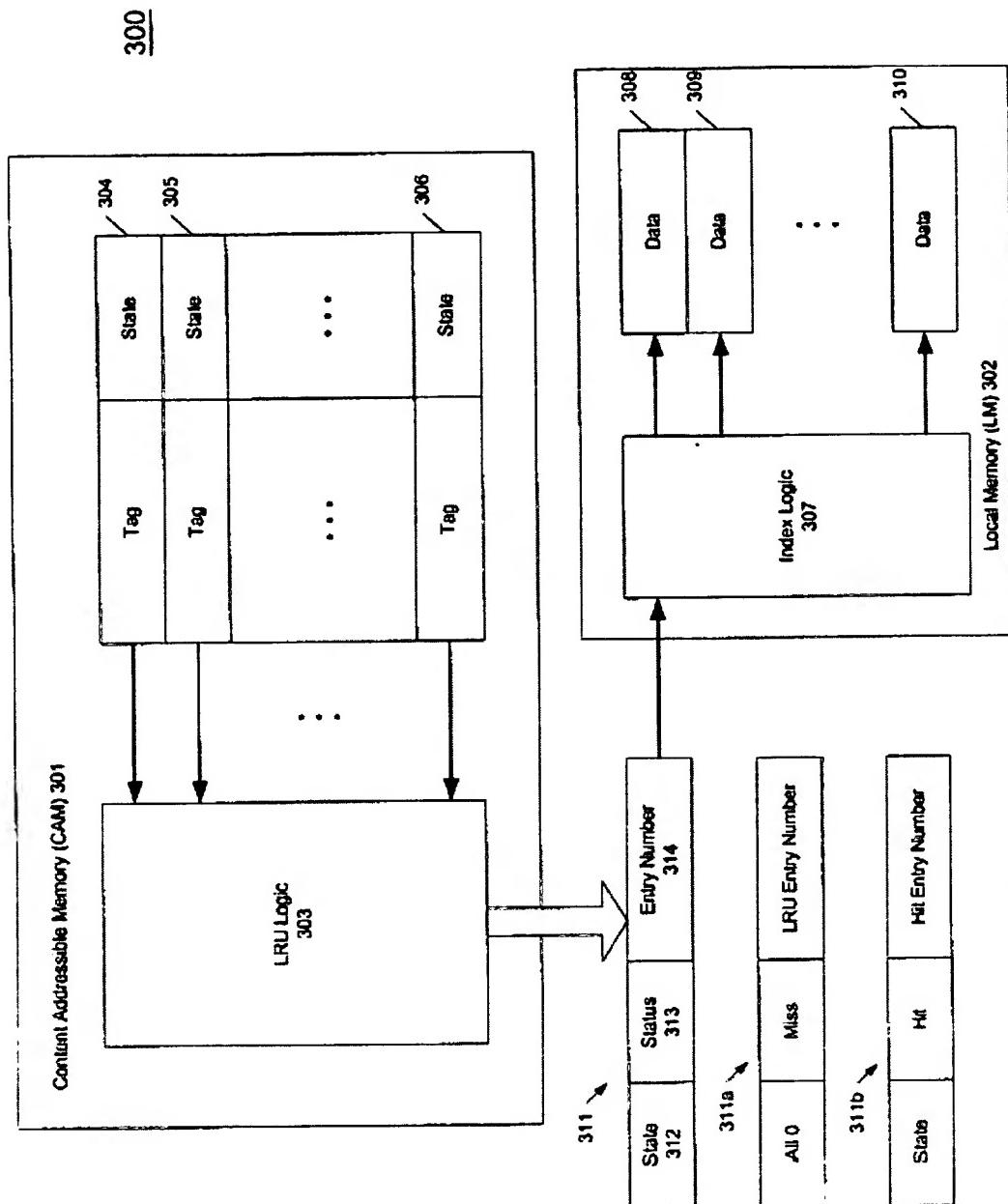


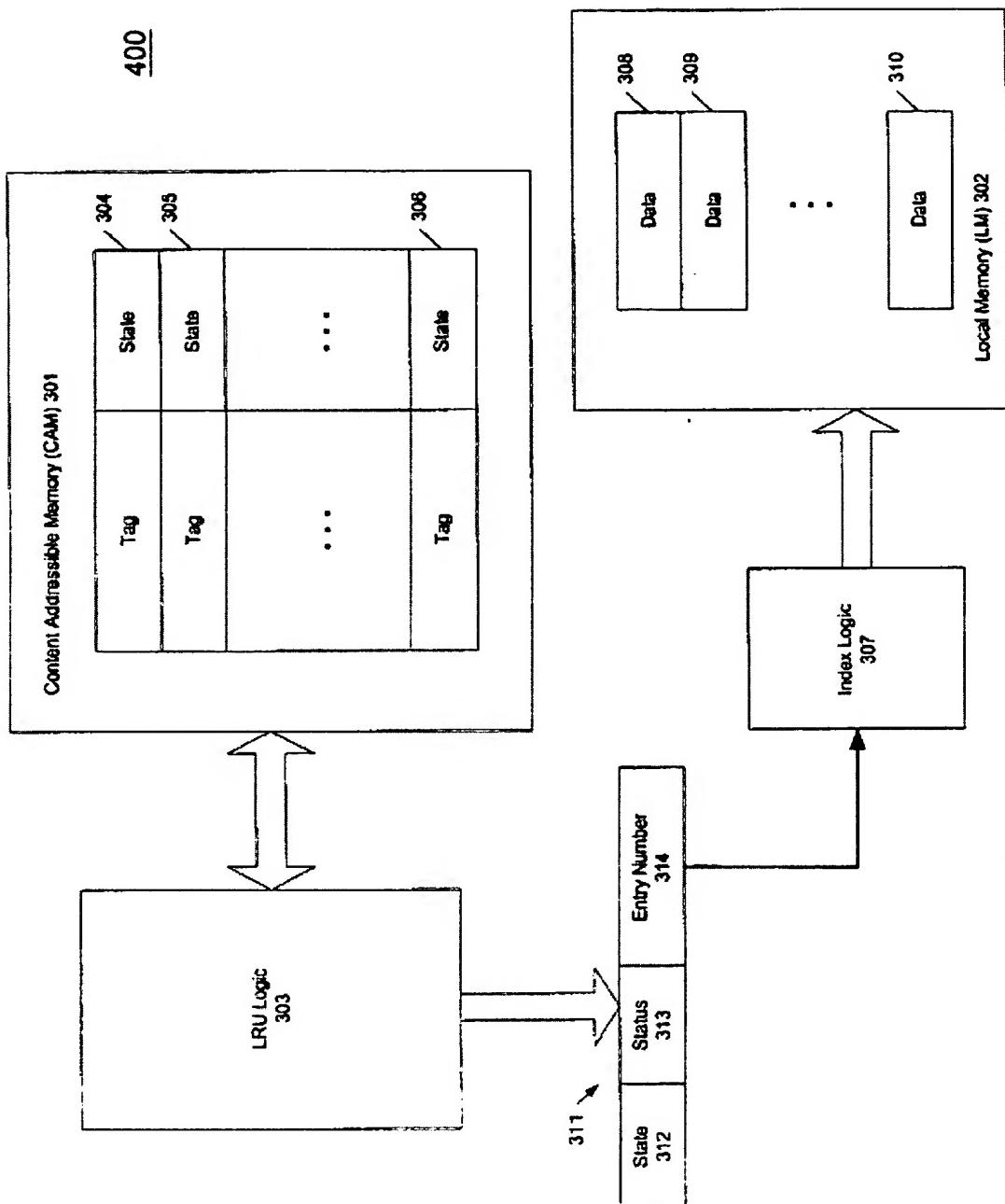
**Fig. 1  
(Prior Art)**

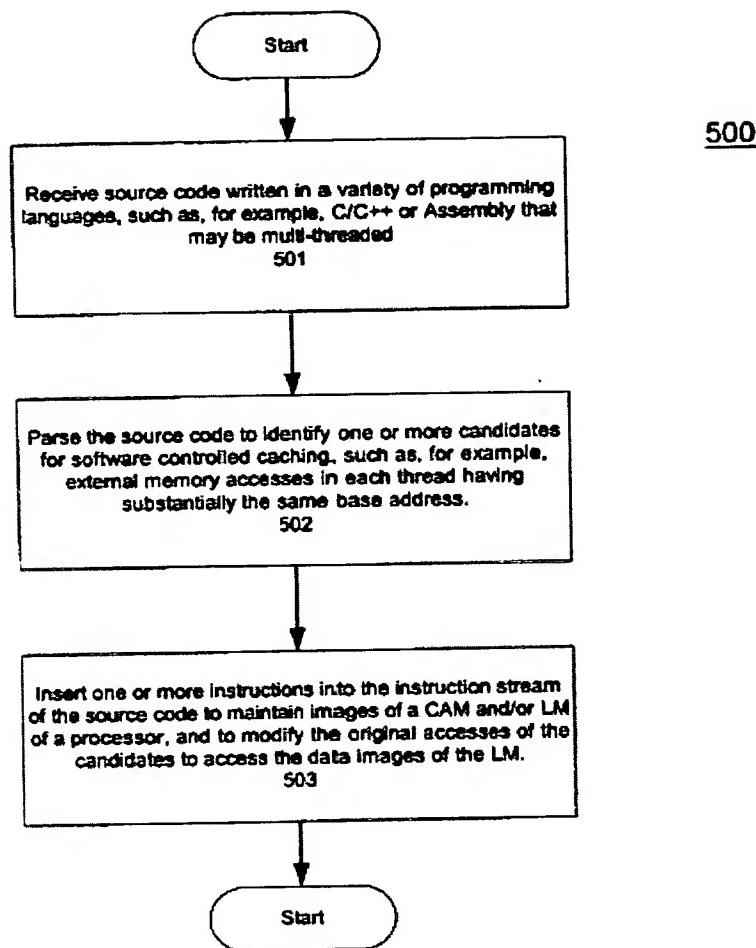
TEST AVAILABLE COPY



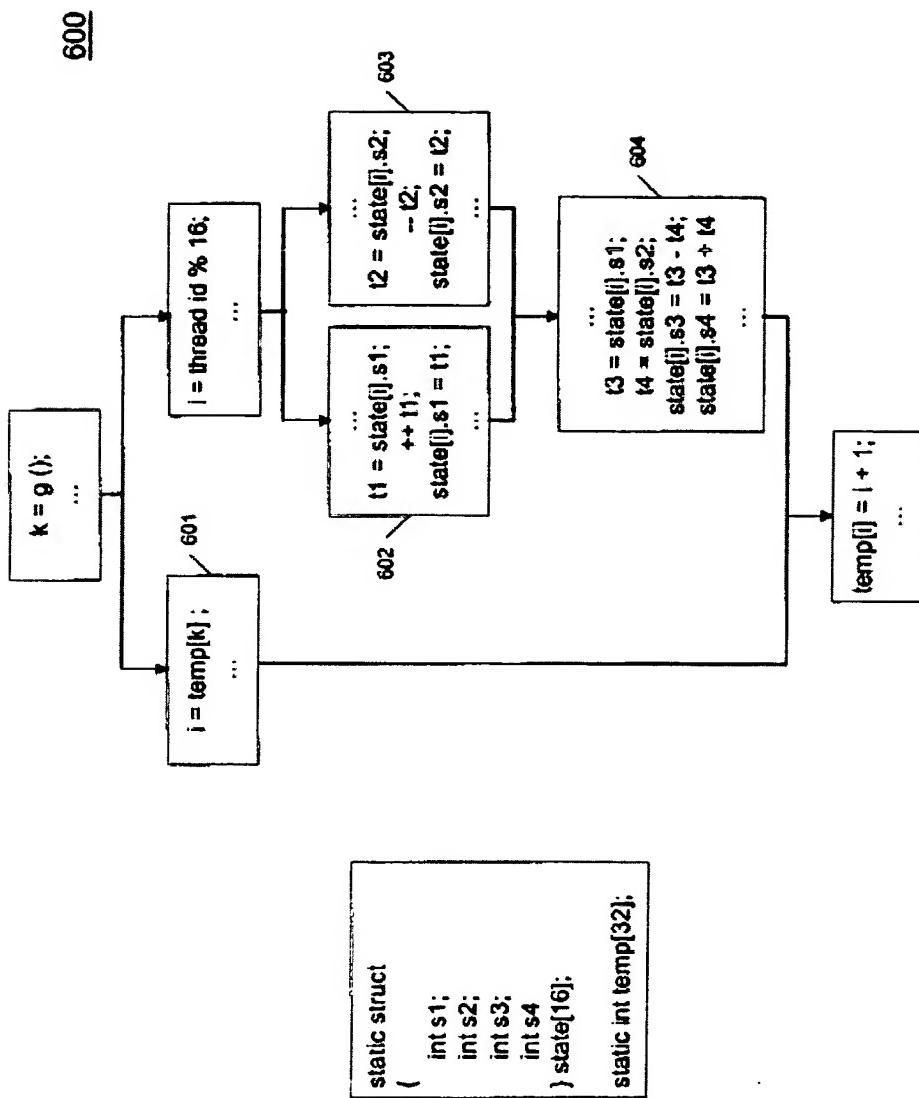
**Fig. 2**

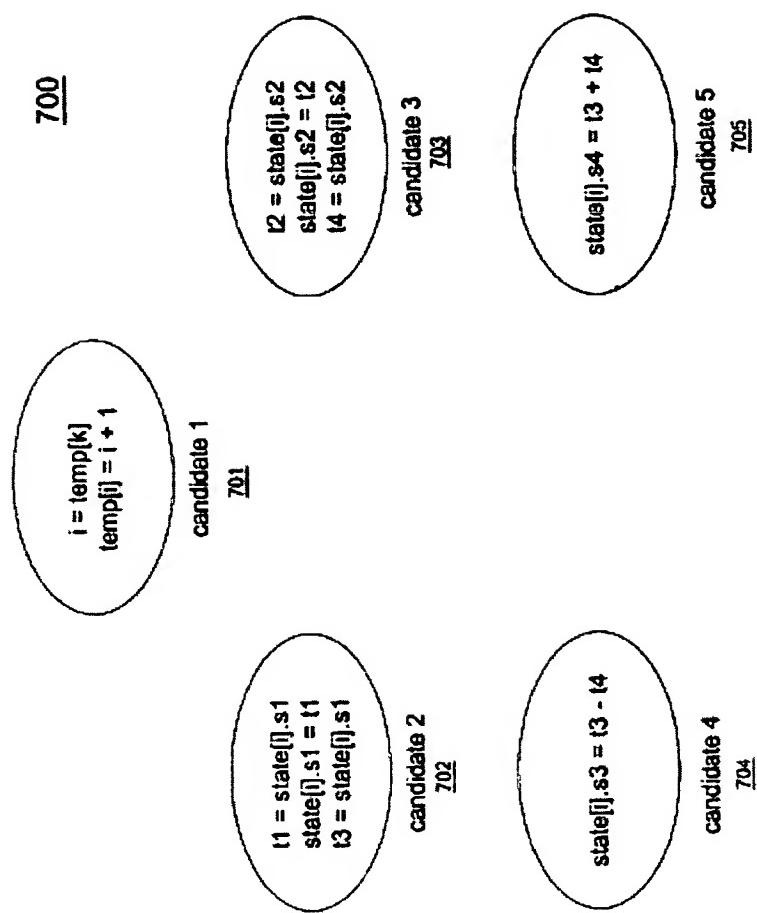
**Fig. 3**

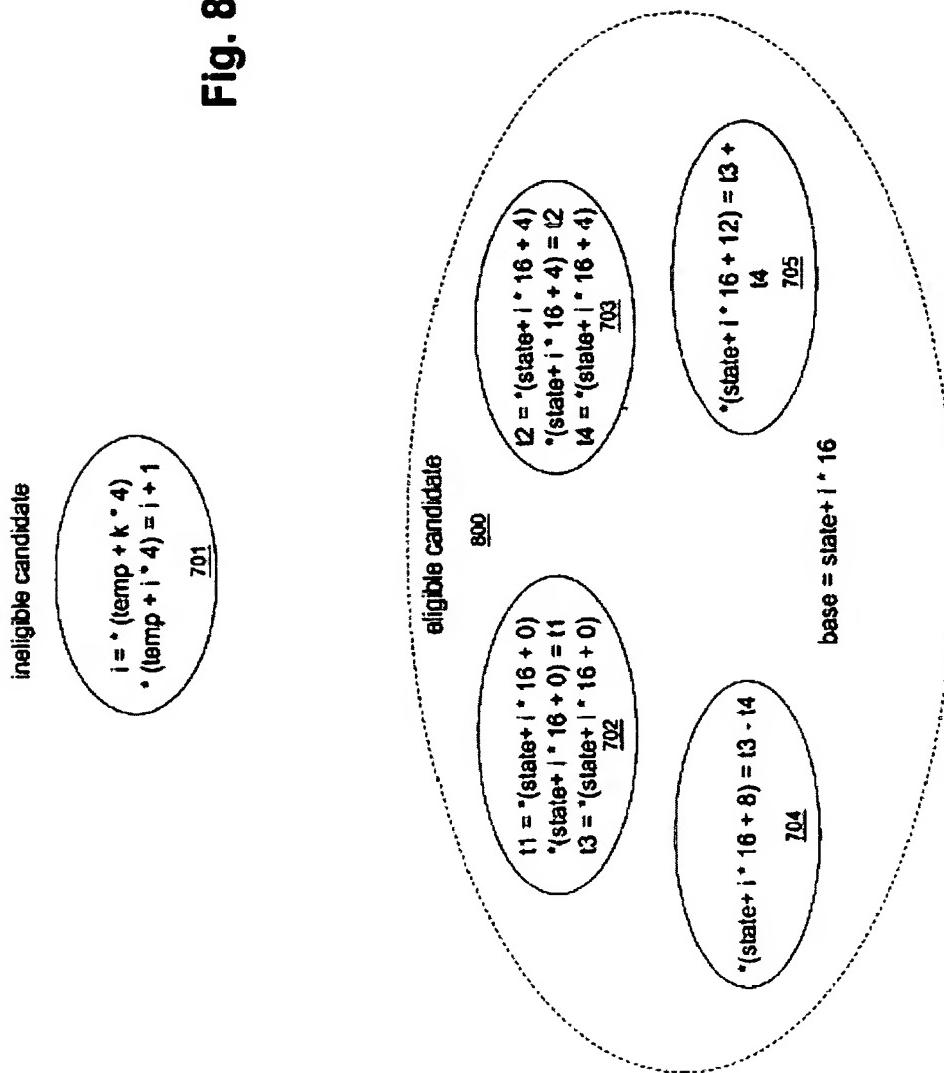
**Fig. 4**

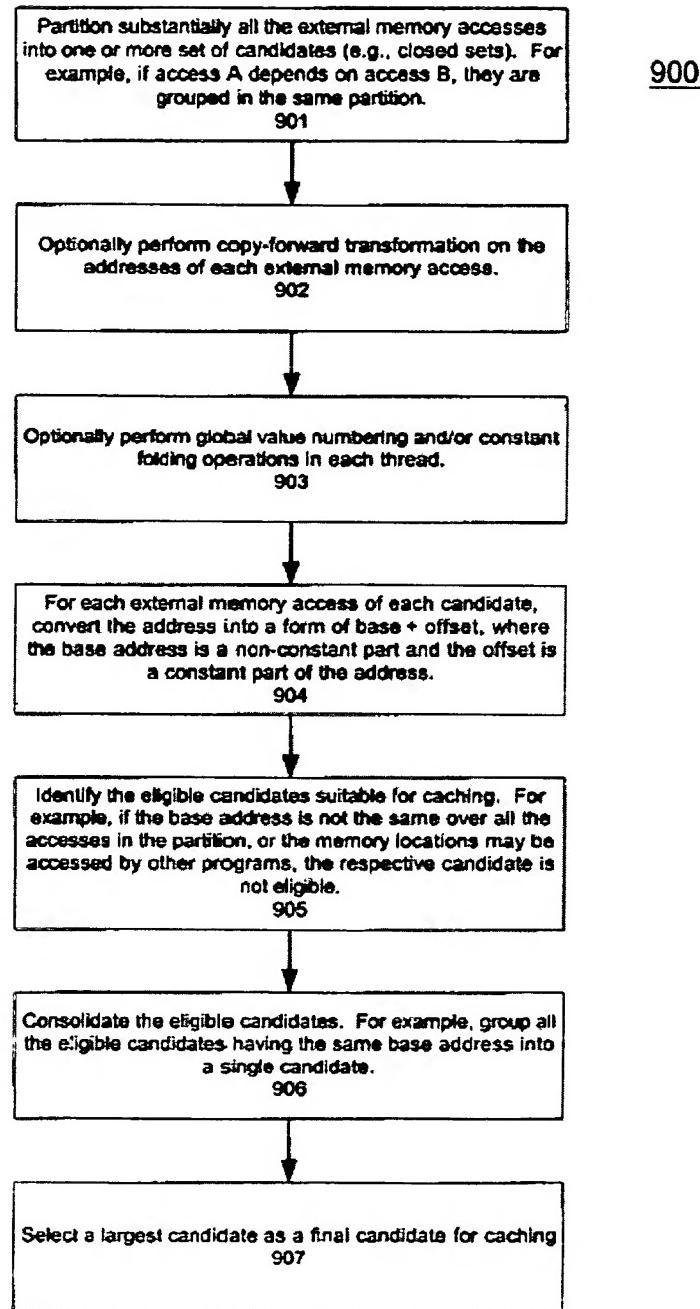


**Fig. 5**

**Fig. 6**

**Fig. 7**

**Fig. 8**

**Fig. 9**

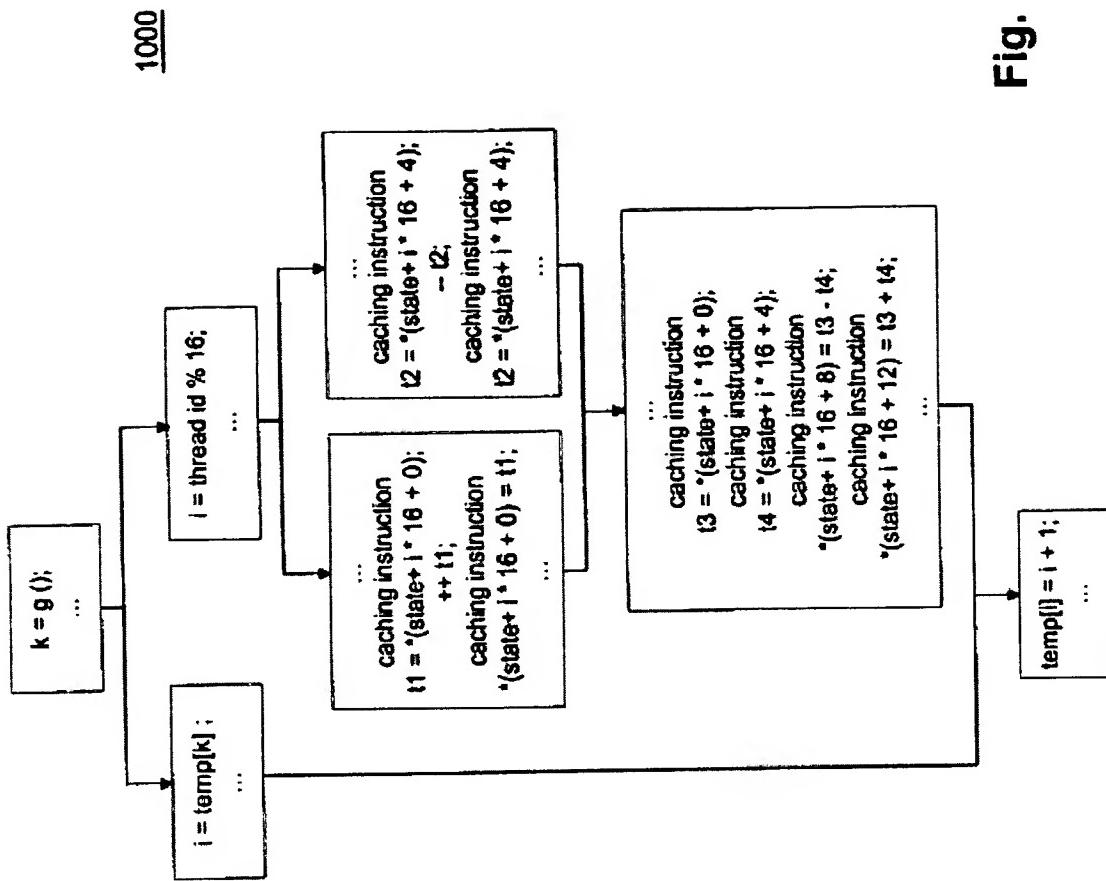
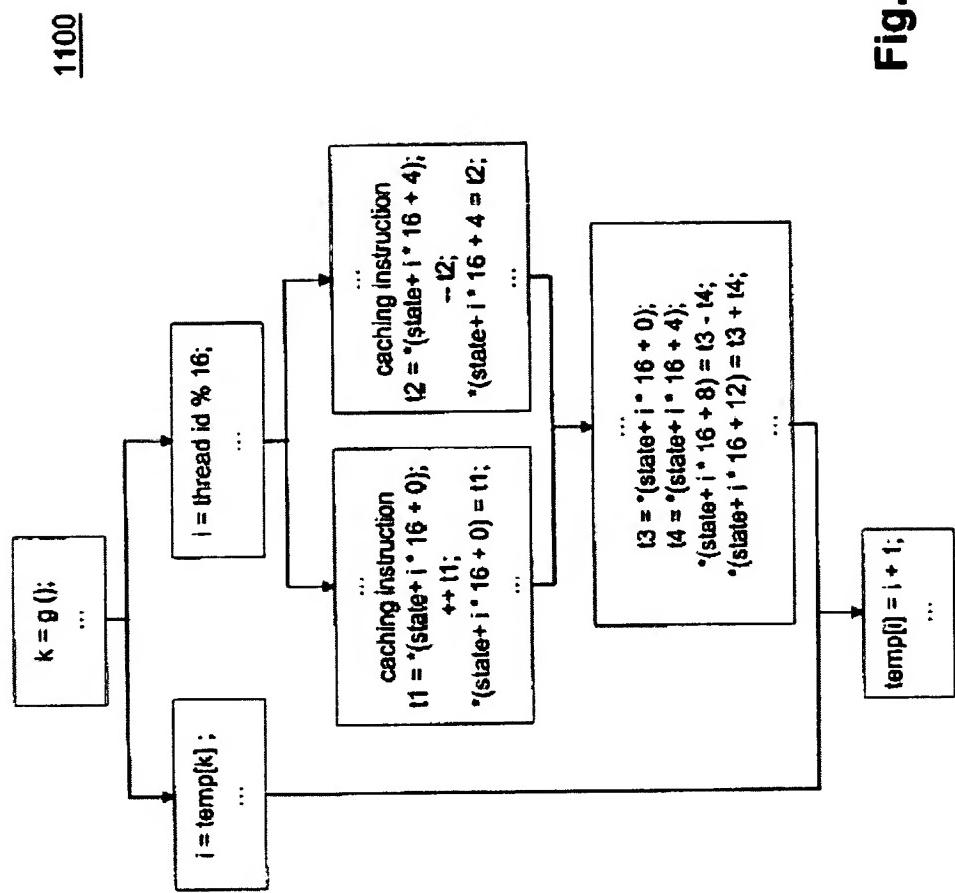


Fig. 10

**Fig. 11**

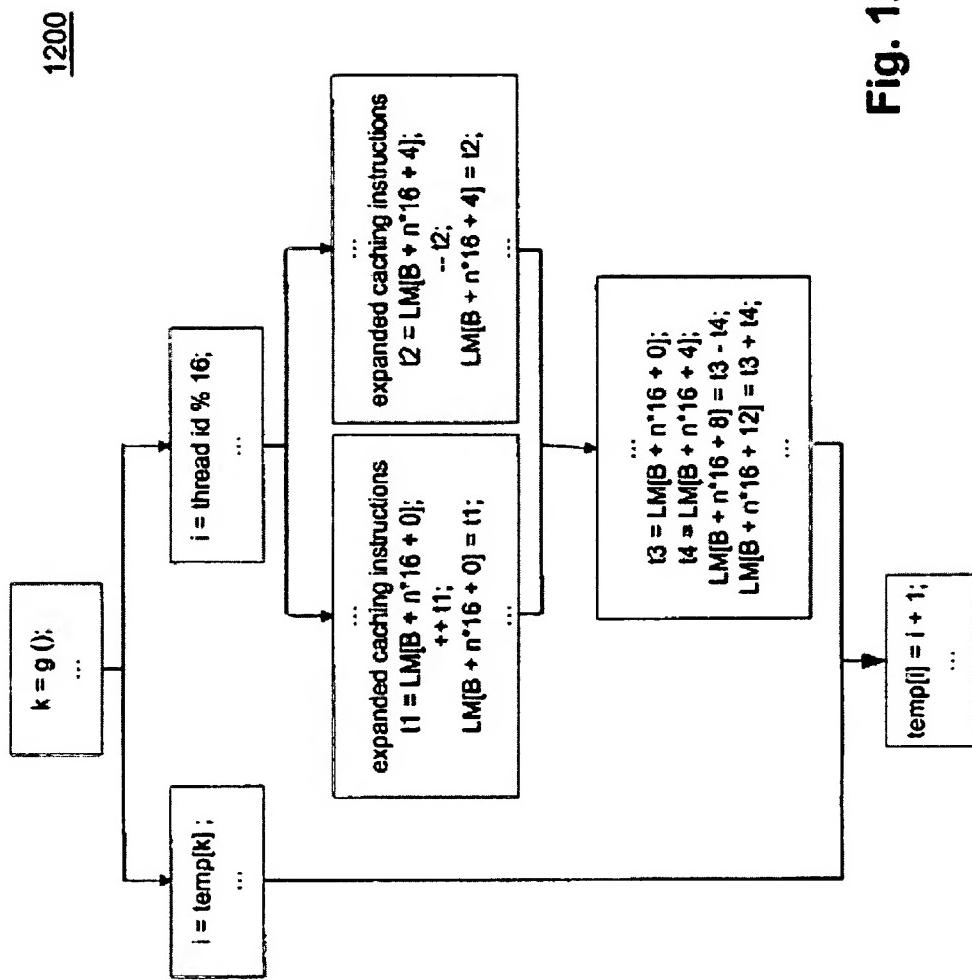
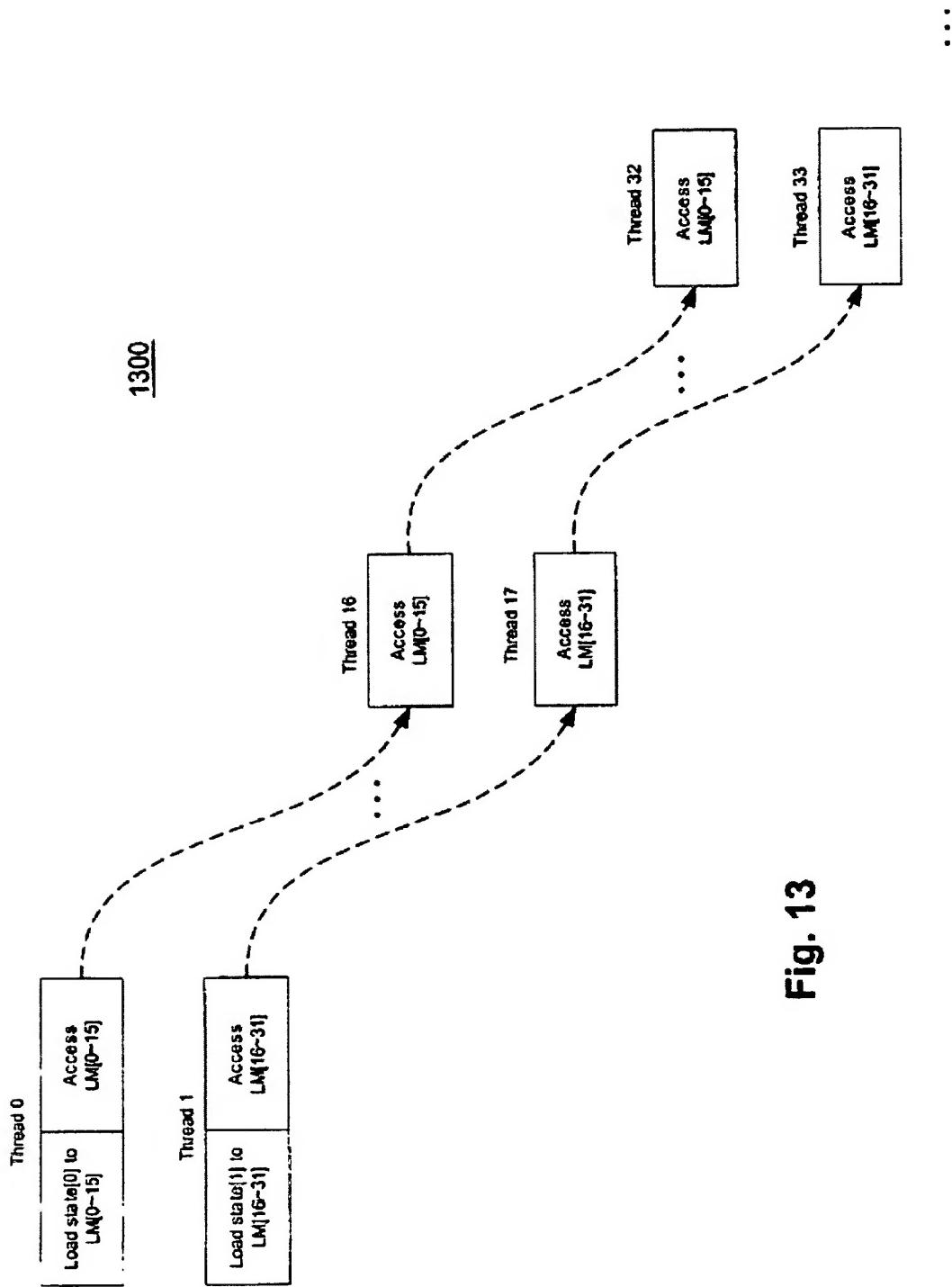


Fig. 12

**Fig. 13**

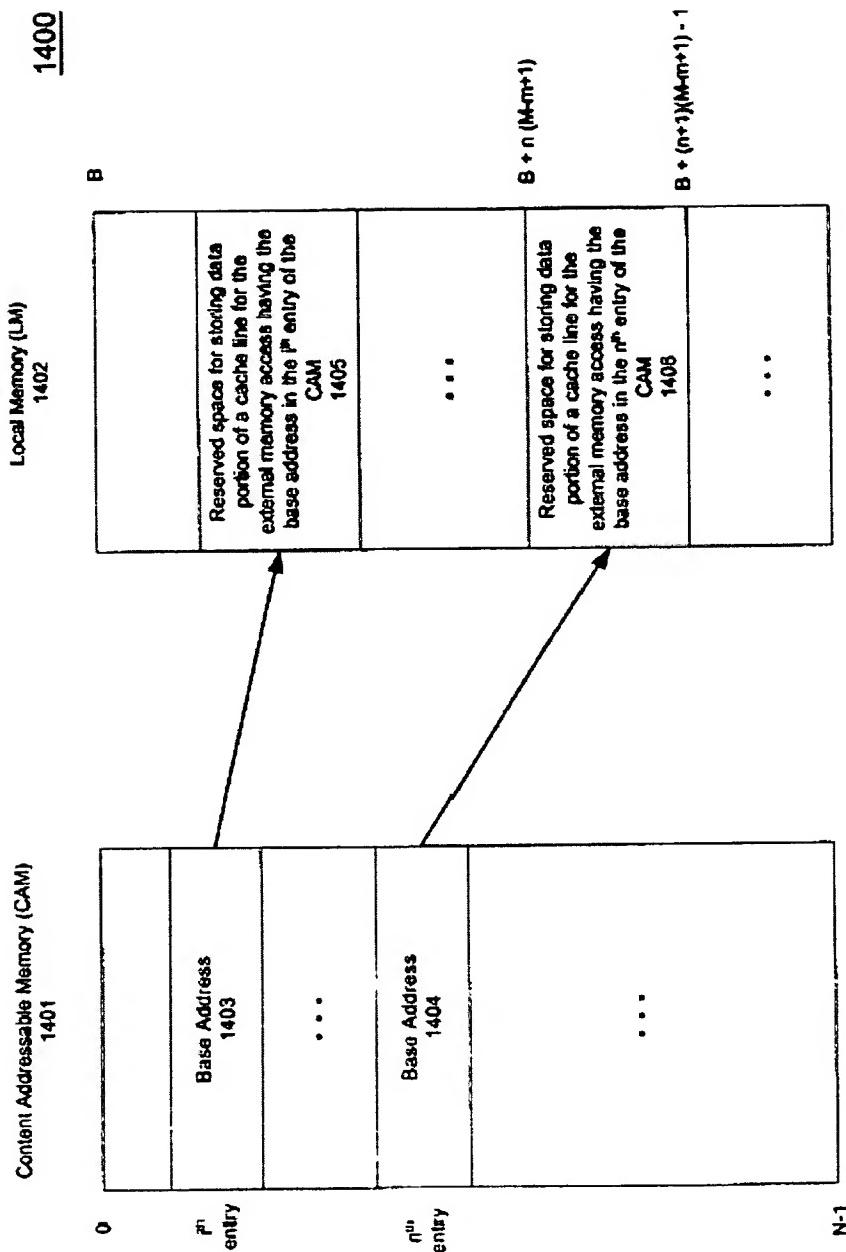


Fig. 14

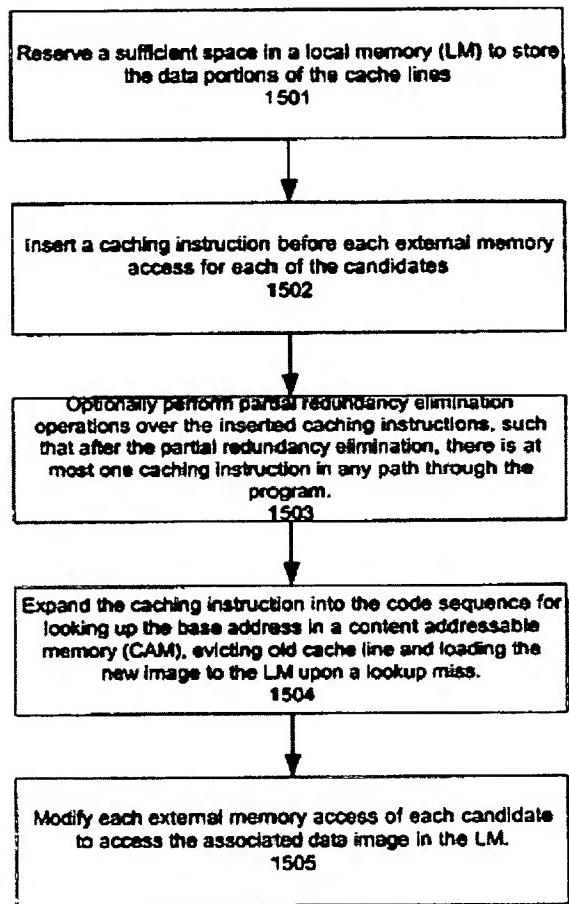
1500

Fig. 15

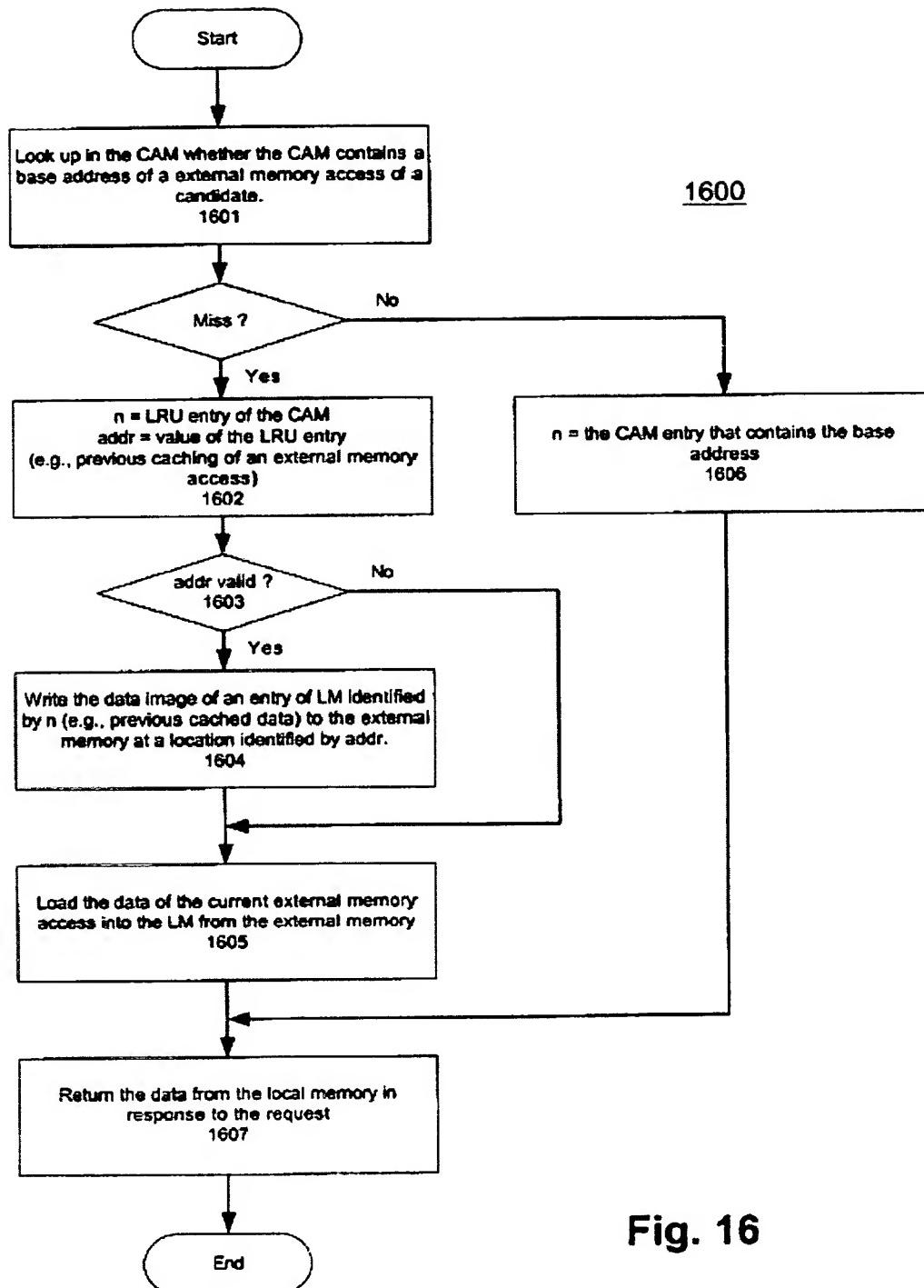


Fig. 16

```

CAM lookup for base
if (lookup miss)
{
    n = the LRU entry in CAM;
    addr = the value contained in the LRU entry in CAM;
    if (addr is valid)
    {
        write the data image in LM (from B + n * (M-m+1) to
        B + (n+1) * (M-m+1) - 1) back to external memory
        (from addr + m to addr + M);
    }
    write base to the rth entry in the CAM;
    load external memory (from base + m to base + M)
    to LM (from B + n * (M-m+1) to B + (n+1) * (M-m+1) - 1);
}
else
{
    n = the associated entry in CAM containing base;
}

```

**Fig. 17**

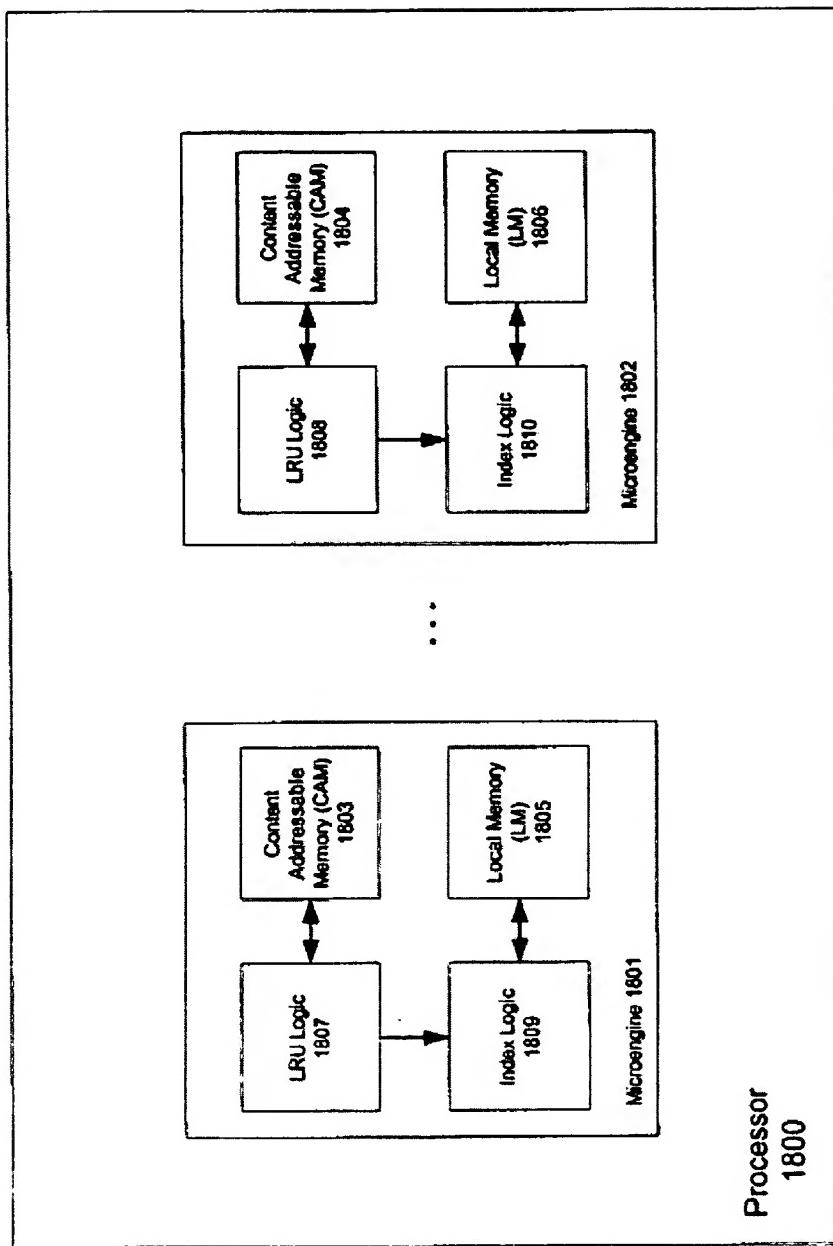


Fig. 18

**Fig. 19**